

TRIBHUVAN UNIVERSITY

2081 (New Course)

Bachelor / Education / 3rd Semester

ICT.Ed.439 Computer Architecture and Organization

Full Marks: 40

Time: 3 hrs.

Candidates are required to give answers in their own words as far as practicable. The figures in the margin indicate full marks.

Attempt all questions

Group "B"

6 X 5 marks = 30

1. Explain different types of data representation in computer with example.

OR

List any two arithmetic, logic and shift micro-operations each with example.

2. Describe basic computer organization with its block diagram.
3. What are the different addressing modes available? Explain each in brief.
4. Find the value of $(-5) * (16)$ by using algorithm to multiply signed magnitude data.

BICTE OR BLOGS

What is pipelining? How instruction pipelining is executed? Explain.

5. What is multiprocessor? Explain Cache Coherence problem in brief.
6. Explain DMA transfer with supportive diagram.

